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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/046,832	01/17/2002	Yong-Jun Kim	401461	5906	
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LEYDIG VOIT & MAYER, LTD			EXAMINER		
700 THIRTEENTH ST. NW SUITE 300			DONG,	DONG, DALEI	
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•			2875		
•			DATE MAILED: 07/21/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

121/

Office Action Summary		Application No.	Applicant(s)
		10/046,832	KIM ET AL.
		Examiner	Art Unit
		Dalei Dong	2875
Period fo	The MAILING DATE of this communication app or Reply	ears on the cover sheet with the	correspondence address
A SH THE - Exte after - If the - If NO - Failu - Any	ORTENED STATUTORY PERIOD FOR REPLY MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. experiod for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period we are to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing end patent term adjustment. See 37 CFR 1.704(b).	within the statutory minimum of thirty (30) da iill apply and will expire SIX (6) MONTHS fror cause the application to become ARANDON	imely filed  lys will be considered timely.  The filling date of this communication.
1)	Responsive to communication(s) filed on 13 J	uno 2002	
2a)□		s action is non-final.	
3)	-,		
	Since this application is in condition for allowa closed in accordance with the practice under E ion of Claims	Ex parte Quayle, 1935 C.D. 11,	453 O.G. 213.
	Claim(s) <u>1-3,5-12 and 14-20</u> is/are pending in the	the application.	
1	4a) Of the above claim(s) is/are withdraw		
	Claim(s) is/are allowed.		
	Claim(s) <u>1-3,5-12 and 14-20</u> is/are rejected.		
	Claim(s) is/are objected to.		
8)[	Claim(s) are subject to restriction and/or	election requirement.	
	on Papers	·	
9) 🗌 .	The specification is objected to by the Examiner.		
10)🖾 -	The drawing(s) filed on 17 January 2002 is/are:	a)⊠ accepted or b)⊡ objected to	by the Examiner.
	Applicant may not request that any objection to the		
11) 🔲 🛚	The proposed drawing correction filed on	is: a)∏ approved b)∏ disappro	oved by the Examiner.
_	If approved, corrected drawings are required in repl	•	
	The oath or declaration is objected to by the Exa	miner.	
Priority u	nder 35 U.S.C. §§ 119 and 120		
	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a	a)-(d) or (f).
a)[	☑ All b) ☐ Some * c) ☐ None of:		
	1. Certified copies of the priority documents	have been received.	
	2. Certified copies of the priority documents	have been received in Applicati	on No. <u>10/046,832</u> .
	3. Copies of the certified copies of the priorit application from the International Bure ee the attached detailed Office action for a list of	eau (PCT Rule 17,2(a)).	_
	cknowledgment is made of a claim for domestic	·	
a)	☐ The translation of the foreign language prov	isional application has been rec	eived.
15)[ A	cknowledgment is made of a claim for domestic	priority under 35 U.S.C. §§ 120	and/or 121.
Attachment			
2) Notice	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal F	(PTO-413) Paper No(s) Patent Application (PTO-152)
S. Patent and Tra PTO-326 (Rev		on Summary	Part of Paper No. 6

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#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-3, 5-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6,489,722 to Yoshida.

Regarding to claims 1-, Yoshida discloses in Figure 1, a "PDP 1 has a pair of substrate structures (including cell constructing elements on a substrate) 10, 20, and has a three-electrode surface discharge structure. In each cell of a screen (a display surface) ES, a pair of display electrodes X, Y and an address electrode A cross each other. The display electrodes X, Y are arranged on the inner surface of a glass substrate 11 of a front substrate structure 10. Each of the display electrodes X, Y has a transparent conductive film 41 that forms a surface discharge gap for each cell and a metal film (a bus conductor) 42 that is overlaid on the middle of the conductive film 41 in the column direction. The metal film 42 is drawn out of the screen ES, so as to connect with a drive circuit. The display electrodes X, Y are covered with a dielectric layer 17 having the thickness of approximately 30-50 .mu.m, and the dielectric layer 17 is coated with a magnesia (MgO) as a protection film 18" (column 4, line 16-31).

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Yoshida also discloses in Figure 1, "the address electrodes A are arranged on the inner surface of a glass substrate 21 of a back substrate structure 20, and are covered with a dielectric layer 24. On the dielectric layer 24, partitions 29 having the height of approximately 150 .mu.m for defining a discharge gas space 31 of two cells in accordance with the present invention. The partition 29 includes a portion for dividing the discharge gas space to columns (hereinafter, referred to as a vertical portion) 291 and a portion for dividing the discharge gas space at an appropriate position in the column direction (hereinafter, referred to as a horizontal portion) 292. Three colors of fluorescent layers 28R, 28G and 28B for color display are arranged to as to cover the inner surface of the back side including the surface of the dielectric layer covering the address electrode A and the side face of the partition 29. The fluorescent layers 28R, 28G and 28B are excited locally by ultraviolet rays emitted by a discharge gas and emit light. Italic characters (R, G and B) in FIG. 1 indicate light emission colors of the fluorescent materials" (column 4, line 32-50).

Yoshida further discloses in Figure 10, "the display electrode Yg includes a transparent conductive film 41g having a tooth-like pattern extending over the entire length of the row and a linear banding metal film 42. The transparent conductive film 41g includes a linear banding base portion 401 and protruding portions 405, 406, 407 defining the discharge portion in each column. Each of the protruding portions 405, 406, 407 is patterned so as to cope from the base portion 401 in T-shape. However, differently from the example of FIG. 6, the areas of protruding portions 405, 406, 407 are optimized in accordance with the light emission color of the corresponding column, so that the

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white balance of the color display can be optimized. In the illustrated example, the  $\underline{\text{width}}$  Wr of the protruding portion 405 in the column whose light emission color is red, the  $\underline{\text{width}}$  Wg of the protruding portion 406 in the column whose light emission color is green, and the  $\underline{\text{width}}$  Wb of the protruding portion 407 in the column whose light emission color is blue have the relationship of Wr < Wg < Wb" (column 7, line 28-46).

It would have been obvious to one having ordinary skill in the art at the time the invention was made to have utilize the display electrode of Yoshida for the plasma display panel of Yoshida in order to optimized in accordance with the light emission color of the corresponding column, so that the white balance of the color display can be optimized.

Regarding to claim 7, Yoshida discloses the claimed invention except for the specific area ratio of different transparent electrodes. It would have been obvious to one having ordarinsy skill in the art at the time the invention was made to have adjust the area ratio of different transparent electrodes in accordance with the design specification, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233.

3. Claims 8-12 and 14-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6,489,722 to Yoshida in view of U.S. Patent No. 6,498,430 to Sakai.

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Regarding to claims 8-11, Yoshida discloses in Figures 1 and 10, a plasma display

panel comprising a rear substrate; a front substrate spaced from the rear substrate and forming a discharge space between the rear and front substrates; partition walls between the front and rear substrates sectioning the discharge space into red, green, and blue discharge cells respectively having coatings of fluorescent substances producing red, green and blue light respectively, so that areas of the discharge cells differ accordance with ratios of efficiencies of light radiation by the respective fluorescent substances; address electrodes on the rear substrate; discharge maintenance electrode, including paris of first and second electrodes, on the front substrate and extending in a direction crossing the address electrodes; and first, second, third transparent electrode extending from the first and second electrodes over at least parts of the red, green and blue discharge cells, respectively, wherein the first, second and third transparent electrode have respective areas differing in accordance with the ratios of efficiencies of light radiation by the fluorescent substances of the red, green and blue discharge cells, located where the first,

However, Yoshida does not discloses the areas of the respectively discharge cells are determined by width of the partition walls surrounding each discharge cell. Sakai teaches in Figures 1 and 2, "In the plasma display device of the present invention, spaces of the light emitting cells 5 are made to have different sizes according to luminance of the fluorescent substance 4. Namely, space of a light emitting cell 5 having fluorescent substance of lower luminance is made larger" (column 3, line 57-61).

second and third transparent electrodes are respectively disposed.

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Sakai also teaches in Figures 1 and 2, "according to the present invention, the light emitting cells are set so that a product of the cube of the size of opening of the light emitting cell of one of primary colors multiplied by luminance of light of the color emitted by the <u>fluorescent</u> substance is substantially equal to that of any other primary color. Preferably, ratio of the opening sizes of cells of different primary colors falls within a range from 0.9 to 1.1 times the 1/3 powers of the ratio of the values of luminance produced by the <u>fluorescent</u> substances of the respective colors. Luminance is determined separately for each of the three kinds of fluorescent substances 4, for R, G and B colors. Luminance of the color of each fluorescent substance may actually be measured using the panel of the plasma display device to be practically used, except for the size of the identical light emitting cells, then obtaining luminance of each single light from the panel which is prepared by applying a fluorescent substance of one color to all light emitting cells in the panel" (column 3, line 62 to column 4, line 12).

Sakai further discloses in Figures 1 and 2, "for the size of the light emitting cell opening of each primary color, ratio of the widths of the openings is changed. For this purpose, ratio of the partition wall pitch and/or ratio of thickness are set for the light emitting cell of each primary color" (column 4, line 13-18).

Sakai further yet teaches in Figures 1 and 2, "The first method is to change the thickness A, B and C of the partition walls 2 which form the light emitting cells 5 with pitches P1, P2 and P3 of the cells 5 of different colors being identical. Thus widths D1, D2 and D3 of the light emitting cells, and consequently the opening areas, are changed" (column 4, line 40-45).

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It would have been obvious to one having ordinary skill in the art at the time the invention was made to have change the thickness of the partition walls of Sakai for the plasma display panel of Yoshida in order to mitigate the deviation in the luminance of each light emitting cells.

Regarding to claim 20, Yoshida discloses the claimed invention except for the specific area ratio of different transparent electrodes. It would have been obvious to one having ordarinsy skill in the art at the time the invention was made to have adjust the area ratio of different transparent electrodes in accordance with the design specification, since it has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233.

## Response to Arguments

4. Applicant's arguments with respect to claims 1-3, 5-12 and 14-20 are have been considered but are moot in view of the new ground(s) of rejection.

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dalei Dong whose telephone number is (703)308-2870. The examiner can normally be reached on 8 A.M. to 5 P.M..

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sandra O'Shea can be reached on (703)305-4939. The fax phone numbers for the organization where this application or proceeding is assigned are (703)872-9318 for regular communications and (703)872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0956.

D.D. July 11, 2003

Sandra O'Shea
Supervisory Patent Examiner
Technology Center 2800